

PMEG4002EB

0.2 A very low $V_{\rm F}$ MEGA Schottky barrier rectifier in SOD523 package

Rev. 02 — 13 January 2010

Product data sheet

1. Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD523 (SC-79) ultra small and flat lead Surface Mounted Device (SMD) plastic package.

1.2 Features

- Forward current: 200 mA
- Reverse voltage: 40 V
- Very low forward voltage
- Ultra small and flat lead SMD plastic package

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications

1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _F	forward current		-	-	200	mA
V _R	reverse voltage		-	-	40	V
V _F	forward voltage	I _F = 200 mA	<u>[1]</u> _	520	600	mV
V_{F}	forward voltage	I _F = 200 mA	<u>[1]</u> -	520	6	600

 $\label{eq:point} \begin{tabular}{ll} \begin{$



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2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline Symbol	
1	cathode	[1]	
2	anode	1 2 1	
		sym001	

[1] The marking bar indicates the cathode.

3. Ordering information

Table 3. C	Ordering	information		
Type number	ər	Package		
		Name	Description	Version
PMEG4002E	В	SC-79	plastic surface mounted package; 2 leads	SOD523

4. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG4002EB	L9

5. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Conditions	Min	Max	Unit
reverse voltage		-	40	V
forward current		-	200	mA
repetitive peak forward current	$t_p \leq 1 \ \text{s}; \ \delta \leq 0.5$	-	300	mA
non-repetitive peak forward current	t _p = 8.3 ms half sine wave; JEDEC method	-	1	A
junction temperature		-	150	°C
ambient temperature		-65	+150	°C
storage temperature		-65	+150	°C
	reverse voltage forward current repetitive peak forward current non-repetitive peak forward current junction temperature ambient temperature	reverse voltage forward current repetitive peak forward current $t_p \le 1 \text{ s}; \delta \le 0.5$ non-repetitive peak forward $t_p = 8.3 \text{ ms half}$ sine wave; JEDEC method junction temperature ambient temperature	reverse voltage-forward current-repetitive peak forward current $t_p \le 1$ s; $\delta \le 0.5$ -non-repetitive peak forward $t_p = 8.3$ ms half sine wave; JEDEC method-junction temperatureambient temperature-65	reverse voltage-40forward current-200repetitive peak forward current $t_p \le 1$ s; $\delta \le 0.5$ -300non-repetitive peak forward $t_p = 8.3$ ms half sine wave; JEDEC method-1junction temperature-150ambient temperature-65+150

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6. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions	I	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1][2]</u> _	-	-	450	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] For Schottky barrier diodes thermal run-away has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and $I_{F(AV)}$ rating will be available on request.

7. Characteristics

Table 7.Characteristics

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified.

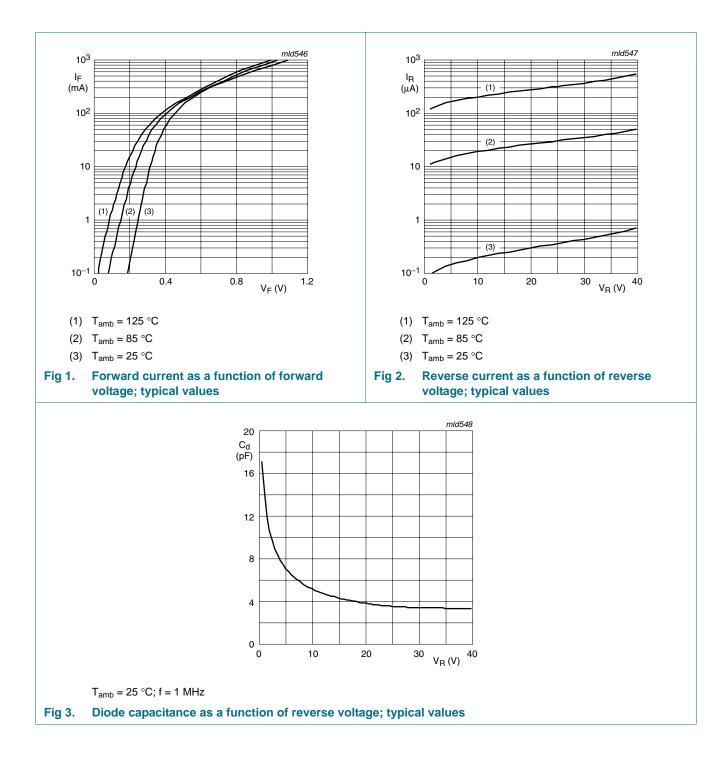
		•				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F for	forward voltage	$I_{F} = 0.1 \text{ mA}$	-	190	220	mV
		I _F = 1 mA	-	250	290	mV
		I _F = 10 mA	-	320	360	mV
		I _F = 100 mA	-	440	500	mV
		I _F = 200 mA	-	520	600	mV
I _R	reverse current	V _R = 25 V	<u>[1]</u> _	-	0.5	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz	-	-	20	pF

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

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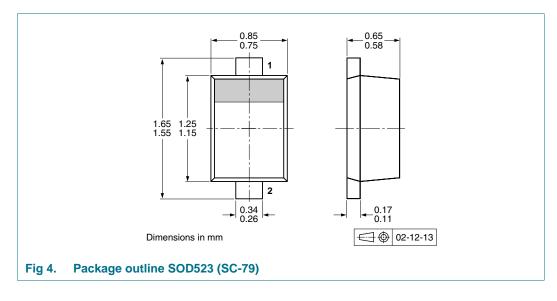
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8. Package outline



9. Packing information

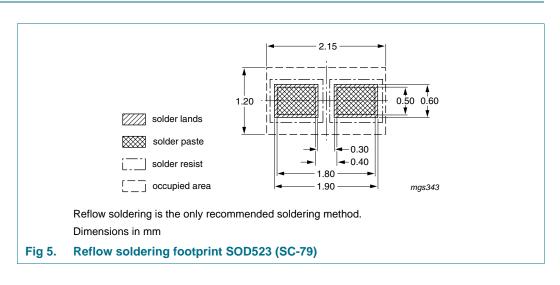
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number Package		Description	Packing	Packing quantity	
			3000	10000	
PMEG4002EB	SOD523	4 mm pitch, 8 mm tape and reel	-115	-135	

[1] For further information and the availability of packing methods, see <u>Section 13</u>.

10. Soldering



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11. Revision history

n history			
Release date	Data sheet status	Change notice	Supersedes
20100113	Product data sheet	-	PMEG4002EB_1
	5		
 Figure 5 "Re 	eflow soldering footprint SC	D523 (SC-79)": update	ed and a second s
20050712	Product data sheet	-	-
	20100113 • This data sh including ne content. • Figure 5 "Re	Release date Data sheet status 20100113 Product data sheet • This data sheet was changed to reflect including new legal definitions and disc content. • Figure 5 "Reflow soldering footprint SC	Release date Data sheet status Change notice 20100113 Product data sheet - • This data sheet was changed to reflect the new company namincluding new legal definitions and disclaimers. No changes we content. • Figure 5 "Reflow soldering footprint SOD523 (SC-79)": update

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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