

**CET**

# CEP840A/CEB840A CEF840A

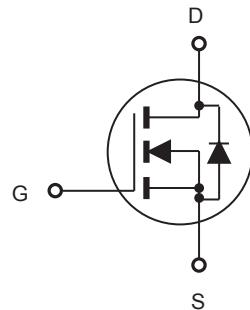
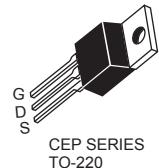
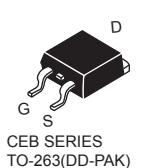
## N-Channel Enhancement Mode Field Effect Transistor

PRELIMINARY

### FEATURES

Type	$V_{DSS}$	$R_{DS(ON)}$	$I_D$	@ $V_{GS}$
CEP840A	500V	0.85Ω	8.5A	10V
CEB840A	500V	0.85Ω	8.5A	10V
CEF840A	500V	0.85Ω	8.5A <sup>d</sup>	10V

- Super high dense cell design for extremely low  $R_{DS(ON)}$ .
- High power and current handing capability.
- Lead-free plating ; RoHS compliant.



### ABSOLUTE MAXIMUM RATINGS $T_C = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Limit		Units
		TO-220/263	TO-220F	
Drain-Source Voltage	$V_{DS}$	500		V
Gate-Source Voltage	$V_{GS}$	$\pm 30$		V
Drain Current-Continuous @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	$I_D$	8.5 6	8.5 <sup>d</sup> 6 <sup>d</sup>	A
Drain Current-Pulsed <sup>a</sup>	$I_{DM}$ <sup>e</sup>	34	34 <sup>d</sup>	A
Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$ - Derate above 25°C	$P_D$	150 1	48 0.3	W W/°C
Single Pulsed Avalanche Energy <sup>h</sup>	$E_{AS}$	196		mJ
Single Pulsed Avalanche Current <sup>h</sup>	$I_{AS}$	7.5		A
Operating and Store Temperature Range	$T_J, T_{stg}$	-55 to 175		°C

### Thermal Characteristics

Parameter	Symbol	Limit		Units
Thermal Resistance, Junction-to-Case	$R_{JC}$	1	3.1	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{JA}$	62.5	65	°C/W

This is preliminary information on a new product in development now .  
Details are subject to change without notice .

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<http://www.cetsemi.com>



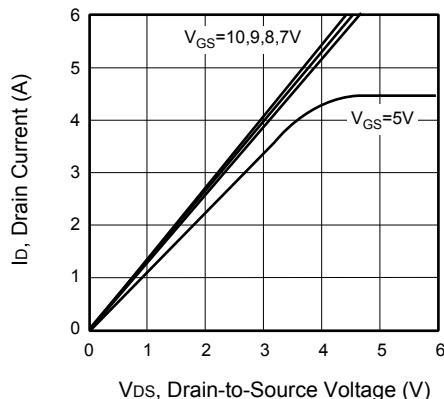
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## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

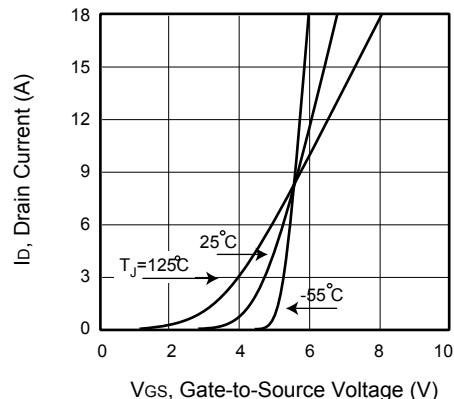
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	500			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 500\text{V}, V_{\text{GS}} = 0\text{V}$		1		$\mu\text{A}$
Gate Body Leakage Current, Forward	$I_{\text{GSSF}}$	$V_{\text{GS}} = 30\text{V}, V_{\text{DS}} = 0\text{V}$		100		nA
Gate Body Leakage Current, Reverse	$I_{\text{GSSR}}$	$V_{\text{GS}} = -30\text{V}, V_{\text{DS}} = 0\text{V}$		-100		nA
<b>On Characteristics<sup>b</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}} = V_{\text{DS}}, I_D = 250\mu\text{A}$	2		4	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 4.3\text{A}$		0.68	0.85	$\Omega$
<b>Dynamic Characteristics<sup>c</sup></b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0 \text{ MHz}$		1580		pF
Output Capacitance	$C_{\text{oss}}$			125		pF
Reverse Transfer Capacitance	$C_{\text{rss}}$			15		pF
<b>Switching Characteristics<sup>c</sup></b>						
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 250\text{V}, I_D = 7\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 9.1\Omega$		20	40	ns
Turn-On Rise Time	$t_r$			5	10	ns
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$			38	76	ns
Turn-Off Fall Time	$t_f$			4	8	ns
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 400\text{V}, I_D = 7\text{A}, V_{\text{GS}} = 10\text{V}$		25	33.3	nC
Gate-Source Charge	$Q_{\text{gs}}$			5		nC
Gate-Drain Charge	$Q_{\text{gd}}$			6		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Drain-Source Diode Forward Current	$I_S^f$				7	A
Drain-Source Diode Forward Voltage <sup>b</sup>	$V_{\text{SD}}^g$	$V_{\text{GS}} = 0\text{V}, I_S = 7\text{A}$			1.5	V
Notes : a.Repetitive Rating : Pulse width limited by maximum junction temperature . b.Pulse Test : Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 2\%$ . c.Guaranteed by design, not subject to production testing. d.Limited only by maximum temperature allowed . e.Pulse width limited by safe operating area . f.Full package $I_{\text{S}(\text{max})} = 4.8\text{A}$ . g.Full package $V_{\text{SD}}$ , test condition $I_S = 4.8\text{A}$ . h.L = 7mH, $ A_S  = 7.5\text{A}$ , $V_{\text{DD}} = 50\text{V}$ , $R_G = 25\Omega$ , Starting $T_J = 25^\circ\text{C}$						

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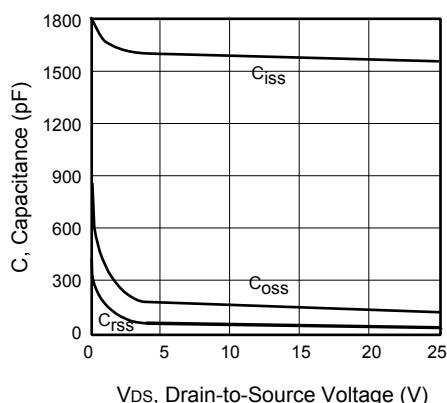
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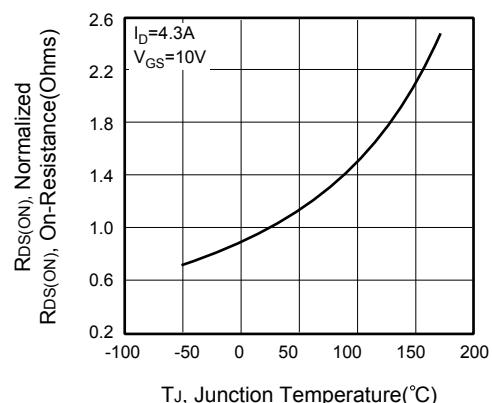
**Figure 1. Output Characteristics**



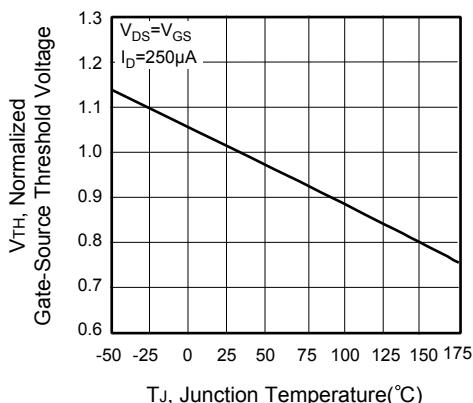
**Figure 2. Transfer Characteristics**



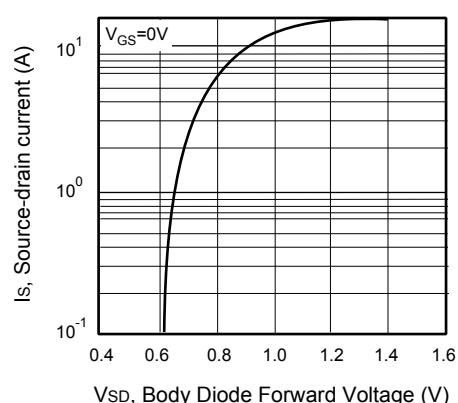
**Figure 3. Capacitance**



**Figure 4. On-Resistance Variation with Temperature**



**Figure 5. Gate Threshold Variation with Temperature**



**Figure 6. Body Diode Forward Voltage Variation with Source Current**

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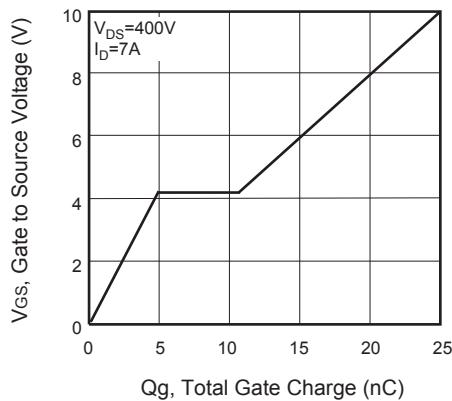


Figure 7. Gate Charge

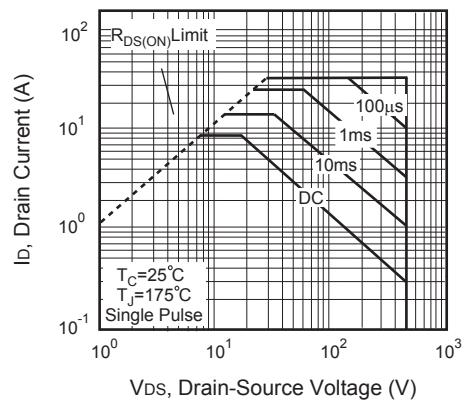


Figure 8. Maximum Safe Operating Area



Figure 9. Switching Test Circuit

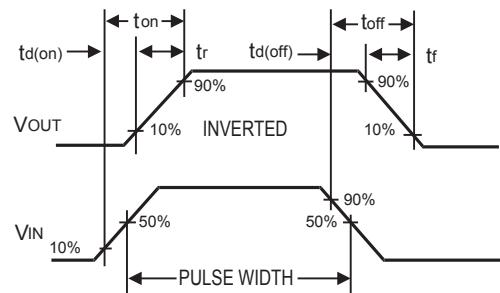


Figure 10. Switching Waveforms

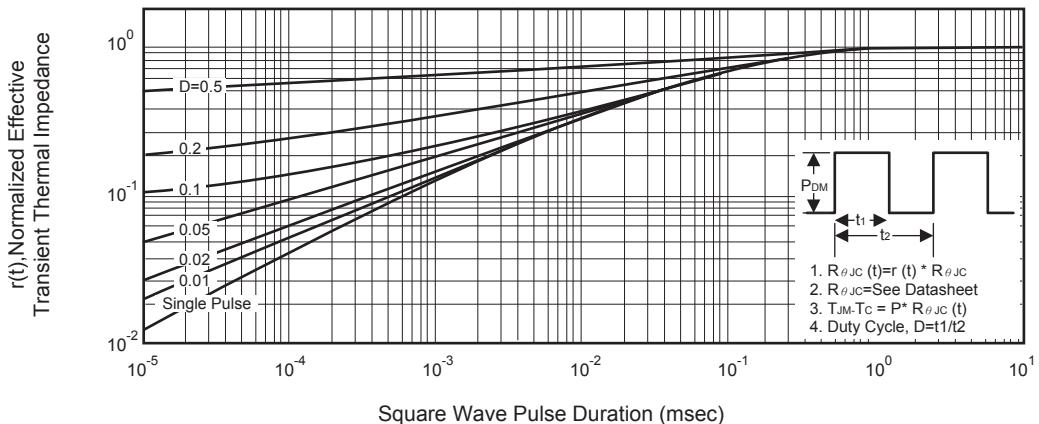


Figure 11. Normalized Thermal Transient Impedance Curve