

Safety Standard Certified Ceramic Capacitors **muRata**

Type KX (Reinforced Insulation) -IEC60384-14 Class X1, Y1-

■ Features

1. Operating temperature range guaranteed up to 125 degrees (UL: 85 deg.).
2. Dielectric strength: AC4000V
3. Class X1/Y1 capacitors certified by UL/CSA/VDE/BSI/SEMKO/DEMKO/FIMKO/NEMKO/ESTI/IMQ.
4. Can be use with a component in appliances requiring reinforced insulation and double insulation based on UL1492, IEC60065 and IEC60950.
5. Coated with flame-retardant epoxy resin (conforming to UL94V-0 standard).
 Please contact us when a halogen-free product* is necessary.
 * Cl=900ppm max., Br=900ppm max. and Cl+Br=1500ppm max.
6. Taping available for automatic insertion.

■ Applications

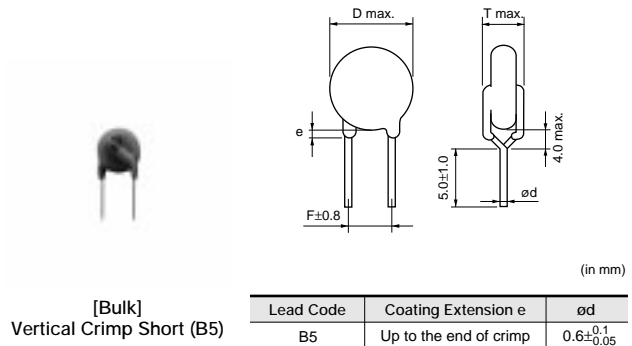
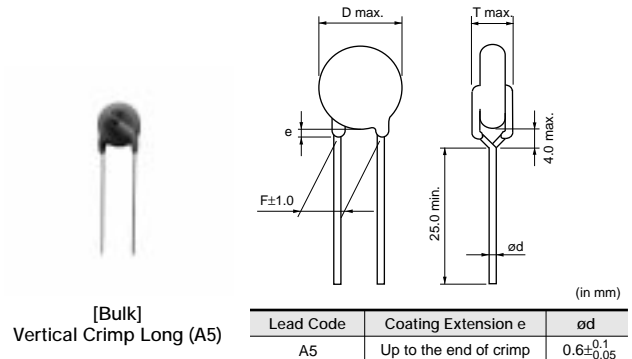
Ideal for use as X/Y capacitors for AC line filters and primary-secondary coupling on switching power supplies and AC adapters.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids. Only Murata products clearly stipulated as "for Automotive use" on its catalog can be used for automobile applications such as power train and safety equipment.

■ Standard Certification

	Standard No.	Certified No.	Rated Voltage
UL	UL1414	E37921	AC250V(r.m.s.)
CSA	CSA E60384-14	1343810	
VDE	IEC 60384-14 EN 60384-14	40002831	
BSI	EN 60065 (8.8, 14.2)	KM 37901	
	IEC 60384-14 EN 60384-14		
SEMKO	IEC 60384-14 EN 60384-14	812158	
DEMKO		314577	
FIMKO		24191	
NEMKO		P08209173	
ESTI		08.0707	
IMQ		V4069	

- The certification number might change due to revision of the application standard and changes in the range of acquisition.
- Please contact us when the certification of Chinese Safety Standard or South Korean Safety Standard is necessary.



■ Marking

Example	Item
	① Type Designation KX
	② Nominal Capacitance (Under 100pF: Actual value, 100pF and over: Marked with 3 figures)
	③ Capacitance Tolerance
	④ Company Name Code ⓄB : Made in Taiwan Ⓞ15 : Made in Thailand
	⑤ Manufactured Date Code
	UL Approval Mark
	CSA Approval Mark
	VDE Approval Mark
	BSI Approval Mark BSI
	SEMKO Approval Mark
	DEMKO Approval Mark
FIMKO Approval Mark	
NEMKO Approval Mark	
ESTI Approval Mark	
IMQ Approval Mark	
Class Code X1Y1	
Rated Voltage Mark 250~	

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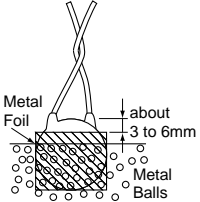
Part Number	AC Rated Voltage (Vac)	Temp. Char.	Capacitance (pF)	Body Dia. D (mm)	Lead Spacing F (mm)	Body Thickness T (mm)	Lead Package Long Bulk	Lead Package Short Bulk	Lead Package Taping (1)
DE11XKX100J□□□	250	SL	10 ±5%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE11XKX150J□□□	250	SL	15 ±5%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE11XKX220J□□□	250	SL	22 ±5%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE11XKX330J□□□	250	SL	33 ±5%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE11XKX470J□□□	250	SL	47 ±5%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE11XKX680J□□□	250	SL	68 ±5%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1B3KX101K□□□	250	B	100 ±10%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1B3KX151K□□□	250	B	150 ±10%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1B3KX221K□□□	250	B	220 ±10%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1B3KX331K□□□	250	B	330 ±10%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1B3KX471K□□□	250	B	470 ±10%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1B3KX681K□□□	250	B	680 ±10%	10 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1E3KX102M□□□A01	250	E	1000 ±20%	8 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1E3KX152M□□□A01	250	E	1500 ±20%	9 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1E3KX222M□□□A01	250	E	2200 ±20%	10 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1E3KX332M□□□A01	250	E	3300 ±20%	12 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1E3KX392M□□□A01	250	E	3900 ±20%	13 max.	10.0	8.0 max.	A5B	B5B	N5A
DE1E3KX472M□□□A01	250	E	4700 ±20%	15 max.	10.0	8.0 max.	A5B	B5B	N5A

Three blank columns are filled with the lead and packaging codes. Please refer to the 3 columns on the right for the appropriate code.

Murata part numbers might be changed depending on lead code or any other changes. Therefore, please specify only the type name (KX) and capacitance of products in the parts list when it is required for applying safety standard of electric equipment.

Type KY/KH/KX Specifications and Test Methods

Operating Temperature Range: -25 to +125°C (-25 to +85°C for UL standards)

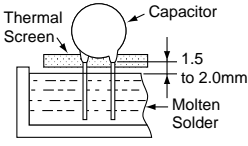
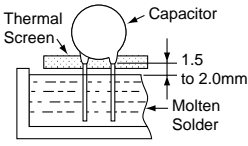
No.	Item	Specifications	Test Method																								
1	Appearance and Dimensions	No visible defect, and dimensions are within specified range.	The capacitor should be visually inspected for evidence of defect. Dimensions should be measured with slide calipers.																								
2	Marking	To be easily legible	The capacitor should be visually inspected.																								
3	Capacitance	Within specified tolerance	The capacitance, dissipation factor and Q should be measured at 20°C with 1±0.1kHz (char. SL: 1±0.1MHz) and AC5V(r.m.s.) max.																								
4	Dissipation Factor (D.F.) Q	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 20%;">Char.</th> <th style="width: 80%;">Specifications</th> </tr> </thead> <tbody> <tr> <td>B, E</td> <td>D.F. ≤2.5%</td> </tr> <tr> <td>F</td> <td>D.F. ≤5.0%</td> </tr> <tr> <td>SL</td> <td>Q ≥ 400+20C*(C<30pF) Q ≥ 1000 (C ≥ 30pF)</td> </tr> </tbody> </table>		Char.	Specifications	B, E	D.F. ≤2.5%	F	D.F. ≤5.0%	SL	Q ≥ 400+20C*(C<30pF) Q ≥ 1000 (C ≥ 30pF)																
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5	Insulation Resistance (I.R.)	10000MΩ min.	The insulation resistance should be measured with DC500±50V within 60±5 sec. of charging. The voltage should be applied to the capacitor through a resistor of 1MΩ.																								
6	Between Lead Wires	No failure	The capacitor should not be damaged when the test voltages from Table 1 are applied between the lead wires for 60 sec. <Table 1> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 20%;">Type</th> <th style="width: 80%;">Test Voltage</th> </tr> </thead> <tbody> <tr> <td>KY</td> <td>For lead spacing F=5mm AC2000V(r.m.s.) For lead spacing F=7.5mm AC2600V(r.m.s.)</td> </tr> <tr> <td>KH</td> <td>AC2600V(r.m.s.)</td> </tr> <tr> <td>KX</td> <td>AC4000V(r.m.s.)</td> </tr> </tbody> </table>	Type	Test Voltage	KY	For lead spacing F=5mm AC2000V(r.m.s.) For lead spacing F=7.5mm AC2600V(r.m.s.)	KH	AC2600V(r.m.s.)	KX	AC4000V(r.m.s.)																
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Dielectric Strength	Body Insulation	No failure	First, the terminals of the capacitor should be connected together. Then, as shown in the figure at right, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 6mm from each terminal.  Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC voltage from Table 2 is applied for 60 sec. between the capacitor lead wires and metal balls. <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 20%;">Type</th> <th style="width: 80%;">Test Voltage</th> </tr> </thead> <tbody> <tr> <td>KY</td> <td>AC2600V(r.m.s.)</td> </tr> <tr> <td>KH</td> <td>AC2600V(r.m.s.)</td> </tr> <tr> <td>KX</td> <td>AC4000V(r.m.s.)</td> </tr> </tbody> </table>	Type	Test Voltage	KY	AC2600V(r.m.s.)	KH	AC2600V(r.m.s.)	KX	AC4000V(r.m.s.)																
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Char.	Capacitance Change																										
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5	20±2																										
8	Solderability of Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of a capacitor should be dipped into molten solder for 2±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C H63 Eutectic Solder 235±5°C																								

*1 "C" expresses nominal capacitance value (pF).

Continued on the following page.

Type KY/KH/KX Specifications and Test Methods

Continued from the preceding page.

No.	Item	Specifications	Test Method								
9	Soldering Effect (Non-Preheat)	Appearance	No marked defect								
		Capacitance Change	Within $\pm 10\%$								
		I.R.	1000M Ω min.								
		Dielectric Strength	Per Item 6								
			<p>As shown in the figure, the lead wires should be immersed in solder of $350\pm 10^\circ\text{C}$ or $260\pm 5^\circ\text{C}$ up to 1.5 to 2.0mm from the root of terminal for 3.5\pm0.5 sec. (10\pm1 sec. for $260\pm 5^\circ\text{C}$).</p> <p>Pre-treatment: Capacitor should be stored at $85\pm 2^\circ\text{C}$ for 1 hr., then placed at room condition*2 for 24\pm2 hrs. before initial measurements.</p> <p>Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2</p> 								
10	Soldering Effect (On-Preheat)	Appearance	No marked defect								
		Capacitance Change	Within $\pm 10\%$								
		I.R.	1000M Ω min.								
		Dielectric Strength	Per Item 6								
			<p>First the capacitor should be stored at $120+0/-5^\circ\text{C}$ for 60+0/-5 sec.</p> <p>Then, as in the figure, the lead wires should be immersed in solder of $260+0/-5^\circ\text{C}$ up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 sec.</p> <p>Pre-treatment: Capacitor should be stored at $85\pm 2^\circ\text{C}$ for 1 hr., then placed at room condition*2 for 24\pm2 hrs. before initial measurements.</p> <p>Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2</p> 								
11	Vibration Resistance	Appearance	No marked defect								
		Capacitance	Within the specified tolerance								
		D.F. Q	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Char.</th> <th>Specifications</th> </tr> </thead> <tbody> <tr> <td>B, E</td> <td>D.F. $\leq 2.5\%$</td> </tr> <tr> <td>F</td> <td>D.F. $\leq 5.0\%$</td> </tr> <tr> <td>SL</td> <td> $Q \geq 400 + 20C^*$ (C < 30pF) $Q \geq 1000$ (C $\geq 30\text{pF}$) </td> </tr> </tbody> </table>	Char.	Specifications	B, E	D.F. $\leq 2.5\%$	F	D.F. $\leq 5.0\%$	SL	$Q \geq 400 + 20C^*$ (C < 30pF) $Q \geq 1000$ (C $\geq 30\text{pF}$)
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	<p>The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10 to 55Hz, 1.5mm in total amplitude, with about a 1-minute rate of vibration change from 10Hz to 55Hz and back to 10Hz.</p> <p>Apply for a total of 6 hrs., 2 hrs. each in 3 mutually perpendicular directions.</p>										
12	Humidity (Under Steady State)	Appearance	No marked defect								
		Capacitance Change	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Char.</th> <th>Capacitance Change</th> </tr> </thead> <tbody> <tr> <td>B</td> <td>Within $\pm 10\%$</td> </tr> <tr> <td>E, F</td> <td>Within $\pm 15\%$</td> </tr> <tr> <td>SL</td> <td>Within $\pm 5\%$</td> </tr> </tbody> </table>	Char.	Capacitance Change	B	Within $\pm 10\%$	E, F	Within $\pm 15\%$	SL	Within $\pm 5\%$
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I.R.	3000M Ω min.										
Dielectric Strength	Per Item 6										
			<p>Set the capacitor for 500\pm12 hrs. at $40\pm 2^\circ\text{C}$ in 90 to 95% relative humidity.</p> <p>Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2</p>								
13	Humidity Loading	Appearance	No marked defect								
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I.R.	3000M Ω min.										
Dielectric Strength	Per Item 6										
			<p>Apply the rated voltage for 500\pm12 hrs. at $40\pm 2^\circ\text{C}$ in 90 to 95% relative humidity.</p> <p>Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2</p>								

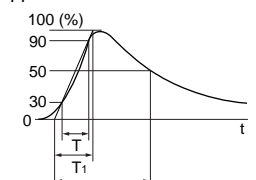
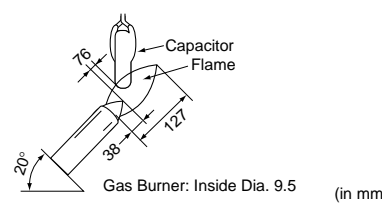
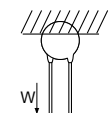
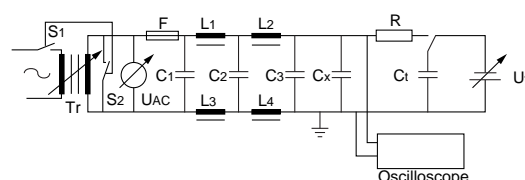
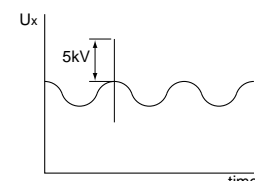
*1 "C" expresses nominal capacitance value (pF).

*2 "Room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa

Continued on the following page.

Type KY/KH/KX Specifications and Test Methods

Continued from the preceding page.

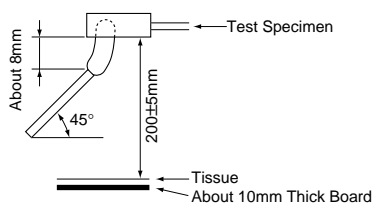
No.	Item	Specifications	Test Method										
14	Life	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%;">Appearance</td> <td>No marked defect</td> </tr> <tr> <td>Capacitance Change</td> <td>Within $\pm 20\%$</td> </tr> <tr> <td>I.R.</td> <td>3000MΩ min.</td> </tr> <tr> <td style="text-align: center;">Dielectric Strength</td> <td style="text-align: center;">Per Item 6</td> </tr> </table>	Appearance	No marked defect	Capacitance Change	Within $\pm 20\%$	I.R.	3000M Ω min.	Dielectric Strength	Per Item 6	<p>Impulse Voltage Each individual capacitor should be subjected to a 5kV (Type KX: 8kV) impulses for three times. Then the capacitors are applied to life test.</p>  <p style="text-align: right;">Front time (T_1) = $1.2\mu\text{s} = 1.67T$ Time to half-value (T_2) = $50\mu\text{s}$</p> <p>Apply a voltage from Table 4 for 1000 hrs. at $125 \pm 2/0^\circ\text{C}$, and relative humidity of 50% max.</p> <p style="text-align: center;"><Table 4></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="text-align: center;">Applied Voltage</th> </tr> <tr> <td style="text-align: center;">AC425V(r.m.s.), except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 sec.</td> </tr> </table> <p>Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.^{*2}</p>	Applied Voltage	AC425V(r.m.s.), except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 sec.
	Appearance	No marked defect											
	Capacitance Change	Within $\pm 20\%$											
	I.R.	3000M Ω min.											
Dielectric Strength	Per Item 6												
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15	Flame Test	<p>The capacitor flame extinguishes as follows.</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 30%;">Cycle</th> <th style="width: 70%;">Time (sec.)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">1 to 4</td> <td style="text-align: center;">30 max.</td> </tr> <tr> <td style="text-align: center;">5</td> <td style="text-align: center;">60 max.</td> </tr> </tbody> </table>	Cycle	Time (sec.)	1 to 4	30 max.	5	60 max.					
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16	Robustness of Terminations	Lead wire should not be cut off. Capacitor should not be broken.	<p>As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10 ± 1 sec.</p> 										
	Bending		<p>Each lead wire should be subjected to 5N of weight and bent 90° at the point of egress, in one direction, then returned to its original position and bent 90° in the opposite direction at the rate of one bend in 2 to 3 sec.</p>										
17	Active Flammability	The cheesecloth should not be on fire.	<p>The capacitor should be individually wrapped in at least one but not more than two complete layers of cheesecloth. The capacitor should be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The UAC should be maintained for 2 min. after the last discharge.</p>  <p> $C_{1,2}$: $1\mu\text{F} \pm 10\%$ C_3 : $0.033\mu\text{F} \pm 5\%$ 10kV $L_{1 \text{ to } 4}$: $1.5\text{mH} \pm 20\%$ 16A Rod core choke C_t : $3\mu\text{F} \pm 5\%$ 10kV R : $100\Omega \pm 2\%$ C_x : Capacitor under test UAC : $U_R \pm 5\%$ F : Fuse, Rated 10A U_R : Rated Voltage U_t : Voltage applied to C_t </p> 										

*2 "Room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa

Continued on the following page.

Type KY/KH/KX Specifications and Test Methods

Continued from the preceding page.

No.	Item	Specifications	Test Method																																																							
18	Passive Flammability	The burning time should not exceed 30 sec. The tissue paper should not ignite.	The capacitor under test should be held in the flame in the position that best promotes burning. Each specimen should only be exposed once to the flame. Time of exposure to flame: 30 sec. Length of flame : 12±1mm Gas burner : Length 35mm min. : Inside Dia. 0.5±0.1mm : Outside Dia. 0.9mm max. Gas : Butane gas Purity 95% min. 																																																							
19	Temperature and Immersion Cycle	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">Appearance</td> <td>No marked defect</td> </tr> <tr> <td style="width: 15%;">Capacitance Change</td> <td> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="width: 10%;">Char.</th> <th>Capacitance Change</th> </tr> <tr> <td>B</td> <td>Within ±10%</td> </tr> <tr> <td>E, F</td> <td>Within ±20%</td> </tr> <tr> <td>SL</td> <td>Within ± 5%</td> </tr> </table> </td> </tr> <tr> <td style="width: 15%;">D.F. Q</td> <td> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="width: 10%;">Char.</th> <th>Specifications</th> </tr> <tr> <td>B, E</td> <td>D.F. ≤5.0%</td> </tr> <tr> <td>F</td> <td>D.F. ≤7.5%</td> </tr> <tr> <td>SL</td> <td> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Q ≥ 275 + 5/2C*1 (C < 30pF)</td> <td style="width: 50%;">Q ≥ 350 (C ≥ 30pF)</td> </tr> </table> </td> </tr> </table> </td> </tr> <tr> <td style="width: 15%;">I.R.</td> <td>3000MΩ min.</td> </tr> <tr> <td style="width: 15%;">Dielectric Strength</td> <td>Per Item 6</td> </tr> </table>	Appearance	No marked defect	Capacitance Change	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="width: 10%;">Char.</th> <th>Capacitance Change</th> </tr> <tr> <td>B</td> <td>Within ±10%</td> </tr> <tr> <td>E, F</td> <td>Within ±20%</td> </tr> <tr> <td>SL</td> <td>Within ± 5%</td> </tr> </table>	Char.	Capacitance Change	B	Within ±10%	E, F	Within ±20%	SL	Within ± 5%	D.F. Q	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="width: 10%;">Char.</th> <th>Specifications</th> </tr> <tr> <td>B, E</td> <td>D.F. ≤5.0%</td> </tr> <tr> <td>F</td> <td>D.F. ≤7.5%</td> </tr> <tr> <td>SL</td> <td> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Q ≥ 275 + 5/2C*1 (C < 30pF)</td> <td style="width: 50%;">Q ≥ 350 (C ≥ 30pF)</td> </tr> </table> </td> </tr> </table>	Char.	Specifications	B, E	D.F. ≤5.0%	F	D.F. ≤7.5%	SL	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Q ≥ 275 + 5/2C*1 (C < 30pF)</td> <td style="width: 50%;">Q ≥ 350 (C ≥ 30pF)</td> </tr> </table>	Q ≥ 275 + 5/2C*1 (C < 30pF)	Q ≥ 350 (C ≥ 30pF)	I.R.	3000MΩ min.	Dielectric Strength	Per Item 6	The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. <div style="text-align: center;"><Temperature Cycle></div> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">Step</th> <th style="width: 60%;">Temperature (°C)</th> <th style="width: 30%;">Time (min)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">1</td> <td style="text-align: center;">-25+0/-3</td> <td style="text-align: center;">30</td> </tr> <tr> <td style="text-align: center;">2</td> <td style="text-align: center;">Room temp.</td> <td style="text-align: center;">3</td> </tr> <tr> <td style="text-align: center;">3</td> <td style="text-align: center;">125+3/-0</td> <td style="text-align: center;">30</td> </tr> <tr> <td style="text-align: center;">4</td> <td style="text-align: center;">Room temp.</td> <td style="text-align: center;">3</td> </tr> </tbody> </table> <p style="text-align: right;">Cycle time: 5 cycles</p> <div style="text-align: center;"><Immersion Cycle></div> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">Step</th> <th style="width: 40%;">Temperature (°C)</th> <th style="width: 15%;">Time (min)</th> <th style="width: 35%;">Immersion Water</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">1</td> <td style="text-align: center;">65+5/-0</td> <td style="text-align: center;">15</td> <td style="text-align: center;">Clean water</td> </tr> <tr> <td style="text-align: center;">2</td> <td style="text-align: center;">0±3</td> <td style="text-align: center;">15</td> <td style="text-align: center;">Salt water</td> </tr> </tbody> </table> <p style="text-align: right;">Cycle time: 2 cycles</p> <p>Pre-treatment: Capacitor should be stored at 85±2°C for 1 hr., then placed at room condition*2 for 24±2 hrs.</p> <p>Post-treatment: Capacitor should be stored for 24±2 hrs. at room condition.*2</p>	Step	Temperature (°C)	Time (min)	1	-25+0/-3	30	2	Room temp.	3	3	125+3/-0	30	4	Room temp.	3	Step	Temperature (°C)	Time (min)	Immersion Water	1	65+5/-0	15	Clean water	2	0±3	15	Salt water
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